

## **ABSTRACT**

For

### **METHOD, SYSTEM AND DEVICE FOR IDENTIFYING A DEFECTIVE UNIT**

The invention concerns a method, a system and an interface circuit for identifying a defective plug-in unit in a computer system comprising a CompactPCI bus (PCI) and plug-in units (2) connected to it via an interface circuit (1). In the method, the bus address being used in the bus is transferred into a first register (A) and the bus address is transferred in conjunction with a reboot from the first register (A) into the second register (B). The bus address can be read for fault diagnosis from the second register (B) by means of an operation and maintenance facility (4). The invention makes it possible to locate faults in situations where bus release is not monitored in conjunction with the addressing sequence. In this situation, e.g. the DEVSEL# signal is active and the TRDY# signal inactive.